

CADETTI Newsletter

Issue #1, August 2026

Italian Competence Centre for Access to Chips Design, Technologies, and Training

Welcome to the first CADETTI Newsletter!

As we wrap up the first six months of the project, we look back on a remarkably productive period. From our kickoff meeting in Rome and intensive brainstorming sessions to structure our First Entry Point services, to selecting our website provider and launching the new CADETTI logo. A key milestone achieved during

the first period was the definition of the governance structure and operational framework of the Competence Centre.

We've hit key milestones right out of the gate. In this inaugural issue, we offer a quick overview of the top takeaway news. Enjoy the read, and make sure to follow us on LinkedIn to stay updated on what's next!



Foreword

Dear Readers,

When time, geopolitics, and shifting demographics collide, hidden vulnerabilities come to light. The world woke up to a stark reality: the bedrock of all global technology - particularly in strategic sectors - rests entirely on semiconductors. But technology is only half the equation. Our true wealth lies in our young talent, yet Italy faces a demographic winter of low birth rates and brain drain, a quiet crisis decades in the making.

To counter these pressures, the European Chips Act and the network of European Competence Centers (CCCs - aCCcess) were born to build true industrial resilience. Within this landscape, CADETTI is stepping up to act as a Competence Centre catalyst in the fields of integrated circuit design and microelectronics.

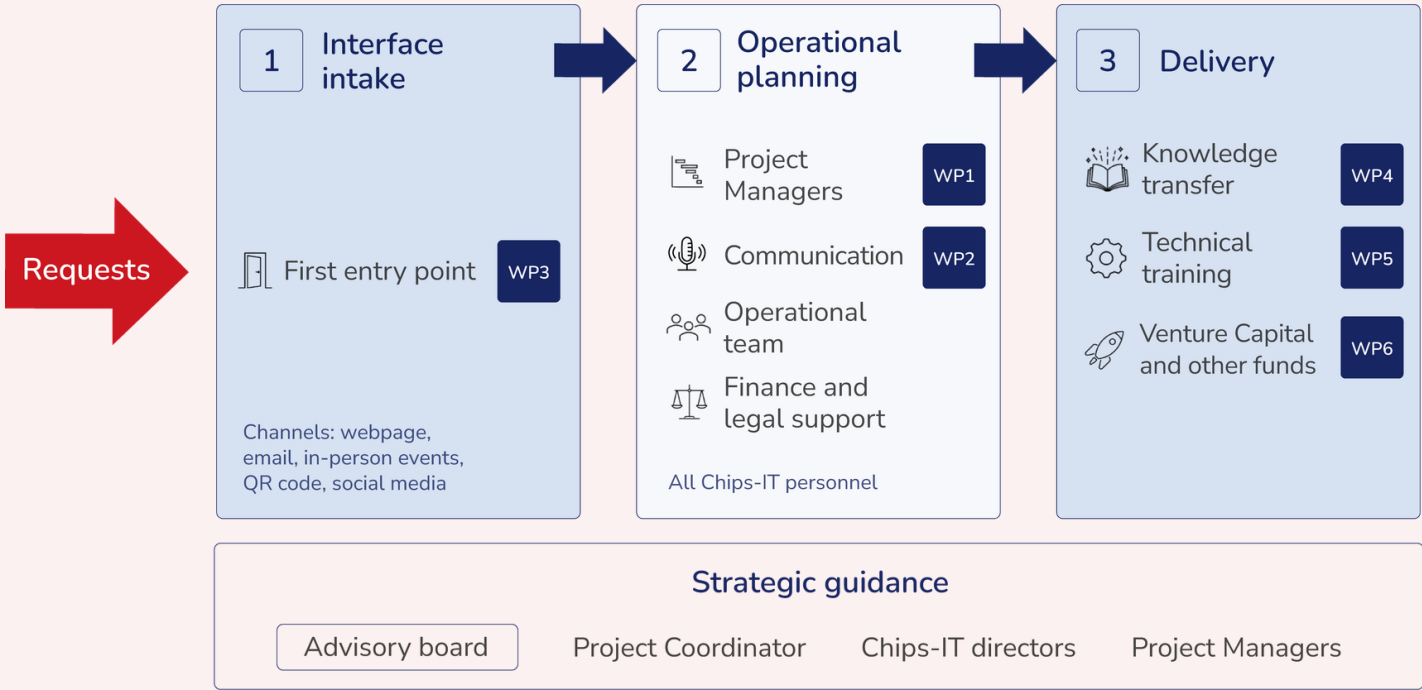
To drive sustainable economic growth, CADETTI is actively building a resilient microelectronics ecosystem. We deliver targeted educational programs to close current skills gaps while empowering tomorrow's innovators. Additionally, we support businesses by facilitating access to cutting-edge circuit design and connecting them with key resources, including funding, VC experts, and industry events.

Thank you for joining us on this journey.
Let's build the future together.

CADETTI Team



Hub governance - process flow



Events

Status	Event name	Link	CADETTI's role	Target group
Completed	ETHZ Winter School	Link	3rd-party event	PhD students and postdocs
	From Micro to Macro	Link	3rd-party event	Unemployed people
	Topics on Microelectronics	Link	3rd-party event	PhD students and postdocs
Upcoming	IEEE COINS 2026	Link	3rd-party event	PhD students and postdocs
	Digital IC Design Training	To be defined	Organizer	Experts from SMEs
	CADETTI and MALTACC Summer School	To be defined	Co-organizer	Master's graduates
	ECS Summer School	Link	3rd-party event	Undergraduate students
	CADETTI Yearly Event 2026	To be defined	Organizer	General public

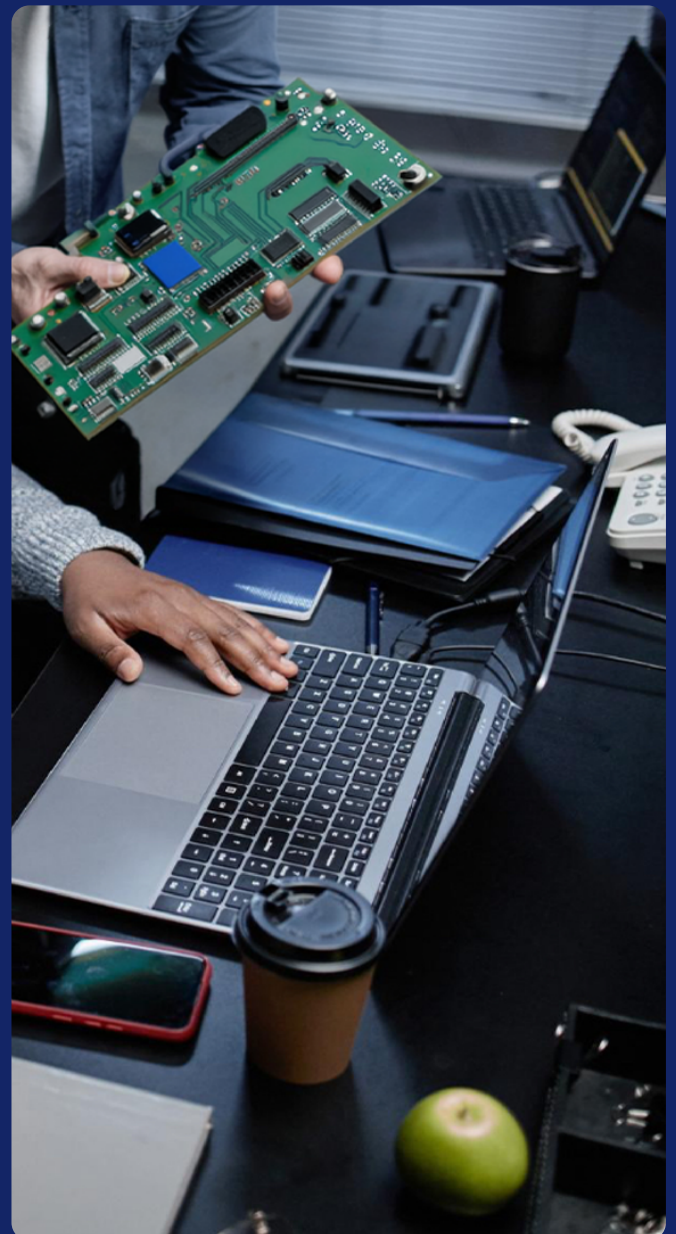
A new digital gateway for the semiconductor ecosystem

A major activity carried out during the first months of CADETTI has been the development of the project website, which will serve as the main entry point for companies and organisations interested in accessing Competence Centre services.

The final logo and website are currently under development and are expected to become operational in September. In parallel, CADETTI's dissemination activities will be strengthened through the launch of the official LinkedIn page in August, providing a dedicated channel to share updates, events and opportunities with the semiconductor community.

[Link to webpage provider:](#)

TeraGlobus



Connecting with the semiconductor community

CADETTI has already started its outreach activities by participating in key national events:



Chips JU Information Day – Mirror Group Italy



[!\[\]\(830769b31eeeaca920791081939ff8ba_img.jpg\) Presentations](#)

The Chips JU 2026 Information Day took place in Rome on February 23, 2026, at the Confindustria Pinin Farina Hall. The event featured interventions by the Executive Director of the Chips JU, Jari Kinaret, as well as representatives from the MUR and MIMIT, who outlined the funding opportunities available in Italy. In particular, the MUR confirmed the allocation of €6 million over three years for RIA projects in Italy, without any regional restrictions.

The afternoon session was dedicated to the presentation of CADETTI, the Italian Competence Centre for Microelectronics, coordinated by Fondazione Chips-IT, which is part of the Chips for Europe Initiative under the Chips JU.



57th Annual Meeting of the Italian Society of Electronics (SIE)



[!\[\]\(0fb13ad0bfa3d86868cdd3883e5665b3_img.jpg\) Event page](#)

In June 2026, CADETTI participated in the annual meeting of the Italian Society of Electronics, presenting the initiative to researchers, academics and industry representatives working in microelectronics, integrated circuits, sensors and electronic systems.

Supporting skills development in semiconductors

During the first months, the project contributed to several training initiatives targeting different groups, including PhD students and researchers, professionals, students interested in semiconductor careers, and people looking for new professional opportunities. The experience collected from these activities will help CADETTI design future training programmes better aligned with industry needs.

Upcoming activities will focus on:

- semiconductor design training,
- upskilling and reskilling programmes,
- collaboration with universities and research organisations increasing awareness of semiconductor careers among young students.



Mapping the Italian semiconductor ecosystem

CADETTI has started the mapping of the Italian semiconductor landscape to better understand capabilities, expertise and opportunities for collaboration.

This mapping activity will support future matchmaking activities and facilitate connections between companies, research organizations and infrastructures.

What's next?

In the next phase, CADETTI will focus on:

Yearly event in Rome 8th October 2026, University La Sapienza;

launching the project website;

publishing the Competence Centre service catalogue;

increasing engagement with SMEs and startups;

organising dedicated training activities;

supporting companies in accessing semiconductor infrastructures and funding opportunities

Join the CADETTI Community and **don't miss**

To stay updated until the webpage will be ready, visit the LinkedIn site.

Stay connected with CADETTI to discover:



upcoming events;



training opportunities;



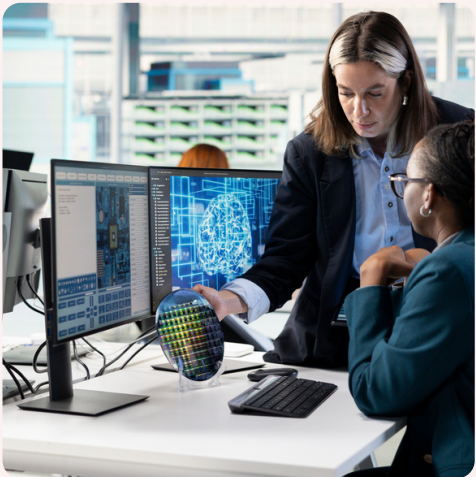
innovation support services;



collaboration opportunities;



newsletters.



Follow us and become part of the Italian semiconductor ecosystem.

Best Regards,
CADETTI Team



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the European Union



Ministero delle Imprese
e del Made in Italy



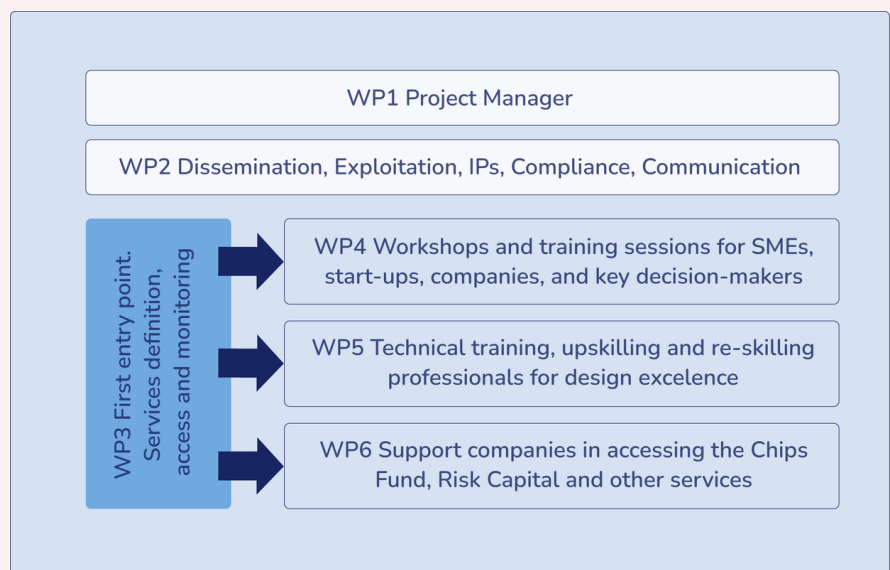
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Apendix

Understanding CADETTI: project structure and key activities

CADETTI is organised around six interconnected Work Packages (WPs), designed to establish and operate the Italian Competence Centre and provide targeted support to the semiconductor ecosystem.

The project structure combines coordination, communication, service delivery and ecosystem development activities:



WP1 – Project Management. Ensures the overall coordination, governance, quality management and smooth implementation of project activities.

WP2 – Communication, Dissemination and Exploitation. Supports the visibility of CADETTI, stakeholder engagement and the promotion of project results, services and opportunities.

WP3 – Service Access and Stakeholder Engagement. Acts as the main entry point for companies, startups, universities and research organisations, guiding users towards the most relevant Competence Centre services and infrastructures.

WP4 – Innovation and Knowledge Transfer. Supports companies in accessing semiconductor expertise, technological capabilities and opportunities for innovation and collaboration.

WP5 – Technical Education and Skills Development. Develops training initiatives aimed at building and strengthening semiconductor skills across different target groups.

WP6 – Financial Support and Business Growth. Provides support in accessing funding opportunities, venture capital and pathways to market for innovative companies.